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22264 U.S. PTO
10/733814



December 12, 2003

Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attorney Reference: COR 136

Re: New Patent Application of: Jui-Neng TU
Title: DUAL DAMASCENE PROCESS FOR FORMING A MULTI-LAYER
LOW-K DIELECTRIC INTERCONNECT

Sir:

Please find attached hereto an application for patent which includes:

- Specification, Claims and Abstract (15 pages)
- 5 Sheets of Formal Drawings (Fig. 1 through Fig. 9)

In view of the above, it is requested that this application be accorded a filing date pursuant to 37 C.F.R. 1.53(b).

A Declaration signed by the inventor, and the filing fee will be submitted in due course.

Respectfully submitted,



Robert H. Berdo, Jr. (Reg. No. 38,075)
(CUSTOMER NO. 23995)

December 12, 2003

Date

RHB:QZ:tz